Network Features

... plug and play interconnect...

... intuitive interfaces ...

... Internet capable ...
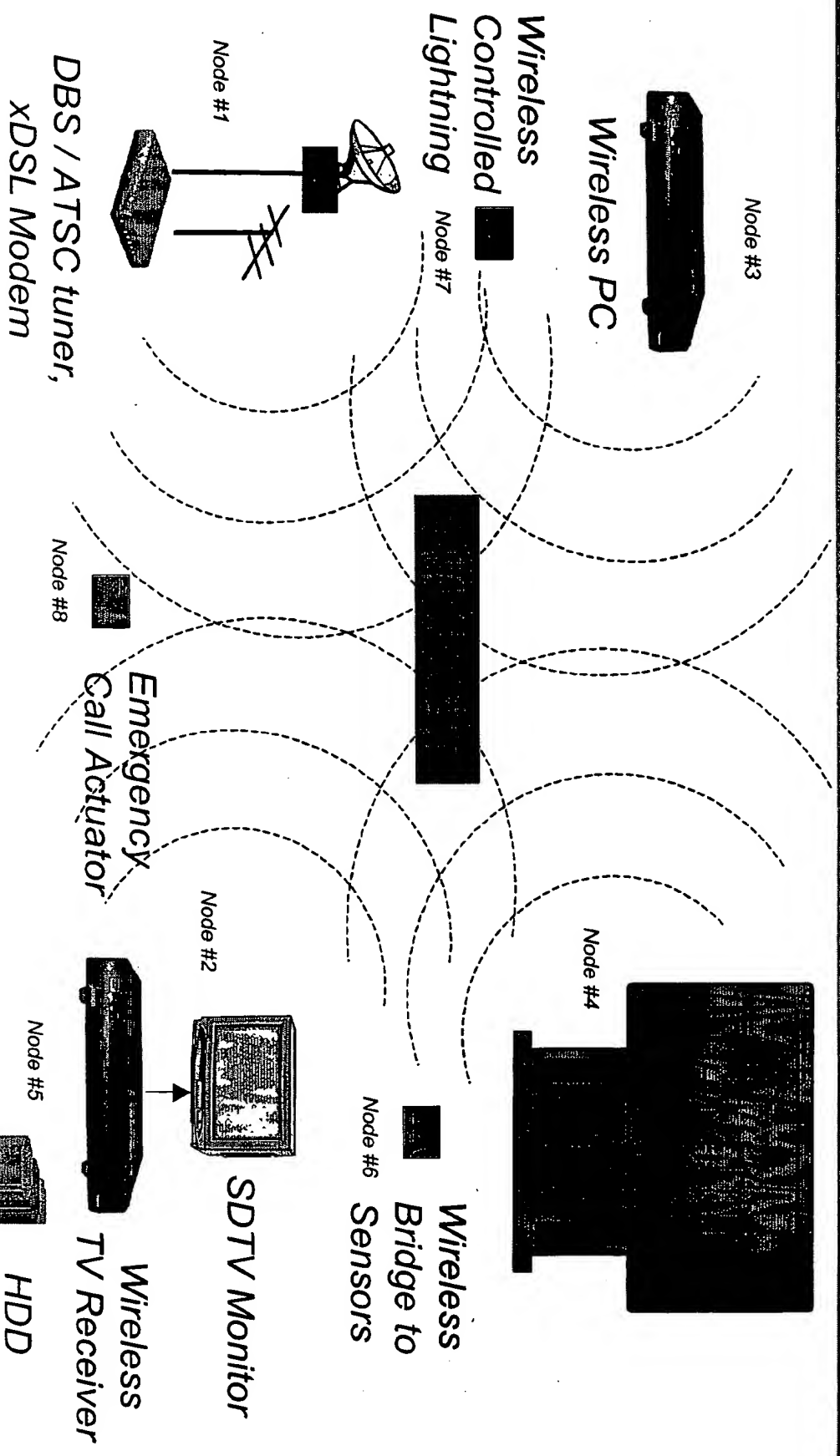
S / ATSC / xDSL and "open cable" suppo

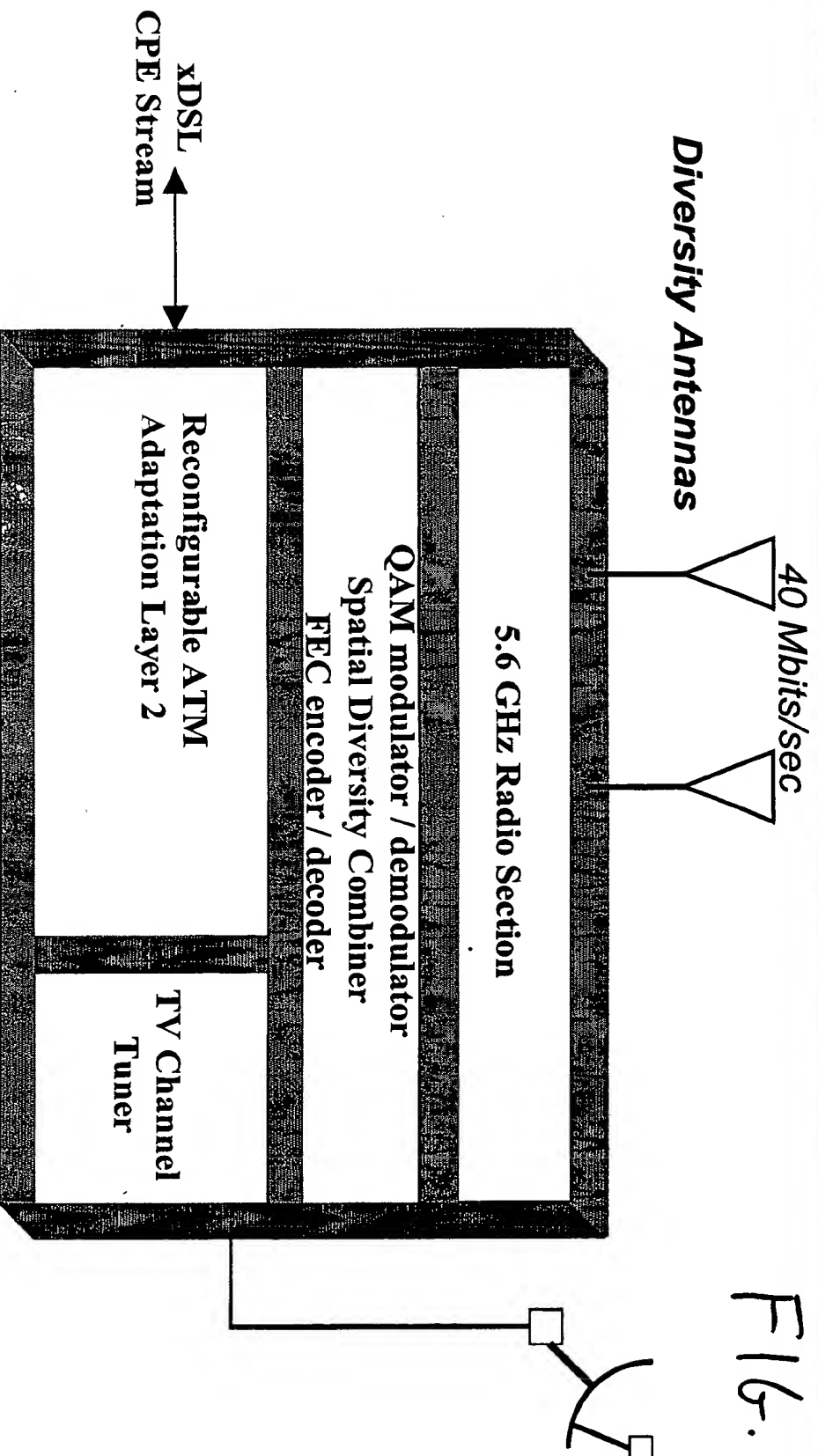...AOL compatible – "*make it easy to use; put it everywhere*" ...

FIG 1

The Network

Node #3

Node #1

Wireless PC

*Wireless
Controlled
Lightning*

Node #7

*DBS / ATSC tuner,
xDSL Modem*

Node #8

*Emergency
Call Actuator*

Node #2

Node #4

Node #5

Node #6

SDTV Monitor

*Wireless
TV Receiver*

*HDD
Upgrade*

*Wireless
Bridge to
Sensors*

FIG. 2

Broadband Home Wireless Portal

Diversity Antennas

40 Mbits/sec

5.6 GHz Radio Section

QAM modulator / demodulator
Spatial Diversity Combiner
FEC encoder / decoder

TV Channel
Tuner

Reconfigurable ATM
Adaptation Layer 2

xDSL
CPE Stream

FIG. 3
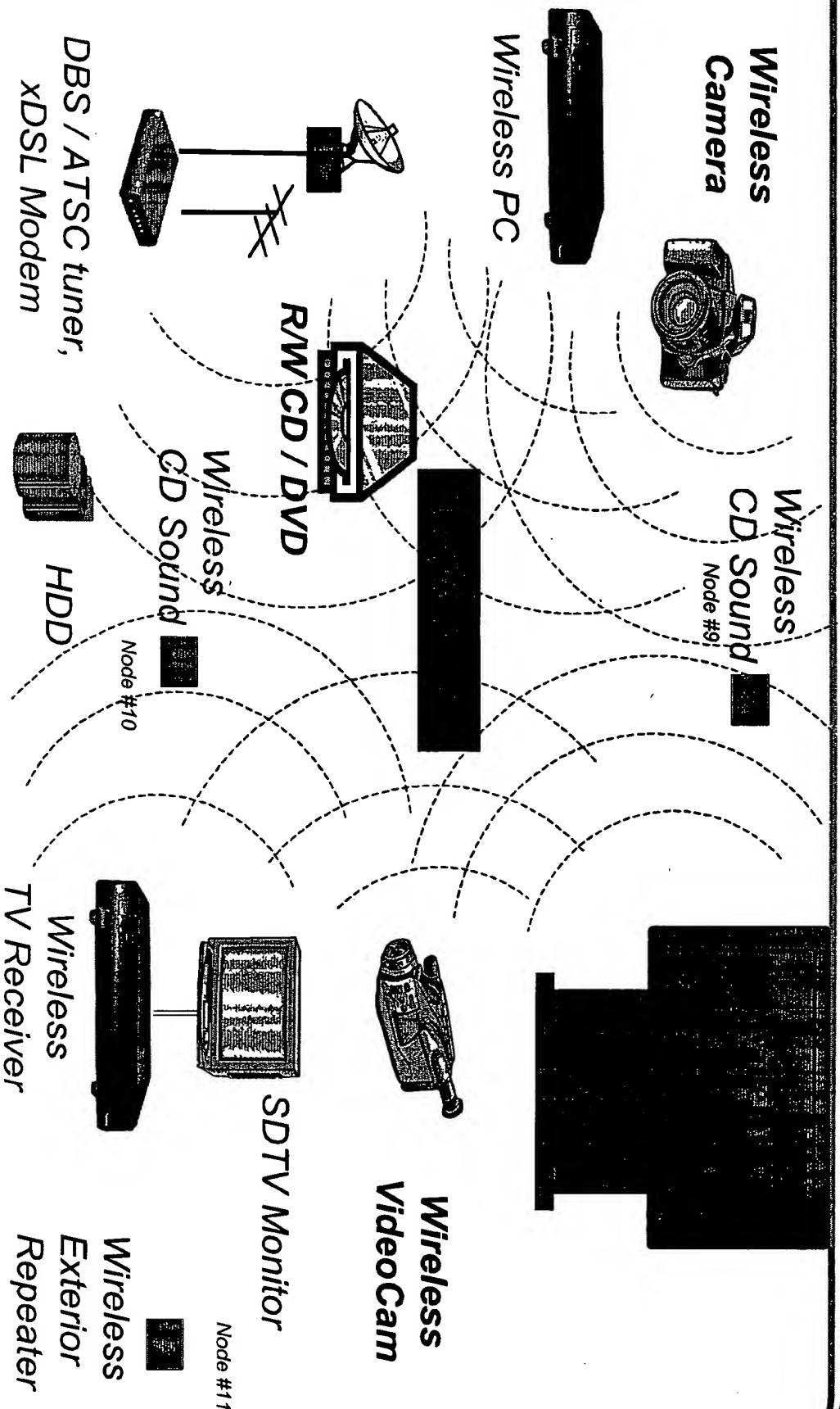
HDD Enables Home Services

FIG. 4

- The HDD becomes the NATURAL repository for still images and video
  … wirelessly accessible from TVs and PCs

  » Enables PVR

  – time shift viewing
  – snap-shot editing / print ordering / picture email
  – video-clip editing / video email
  – datacasting / e-commerce / impulse purchasing
  – indexing by video content / video archiving

  » Support service access via xDSL and / or satellite for home management systems

Expanded Network

FIG. 5

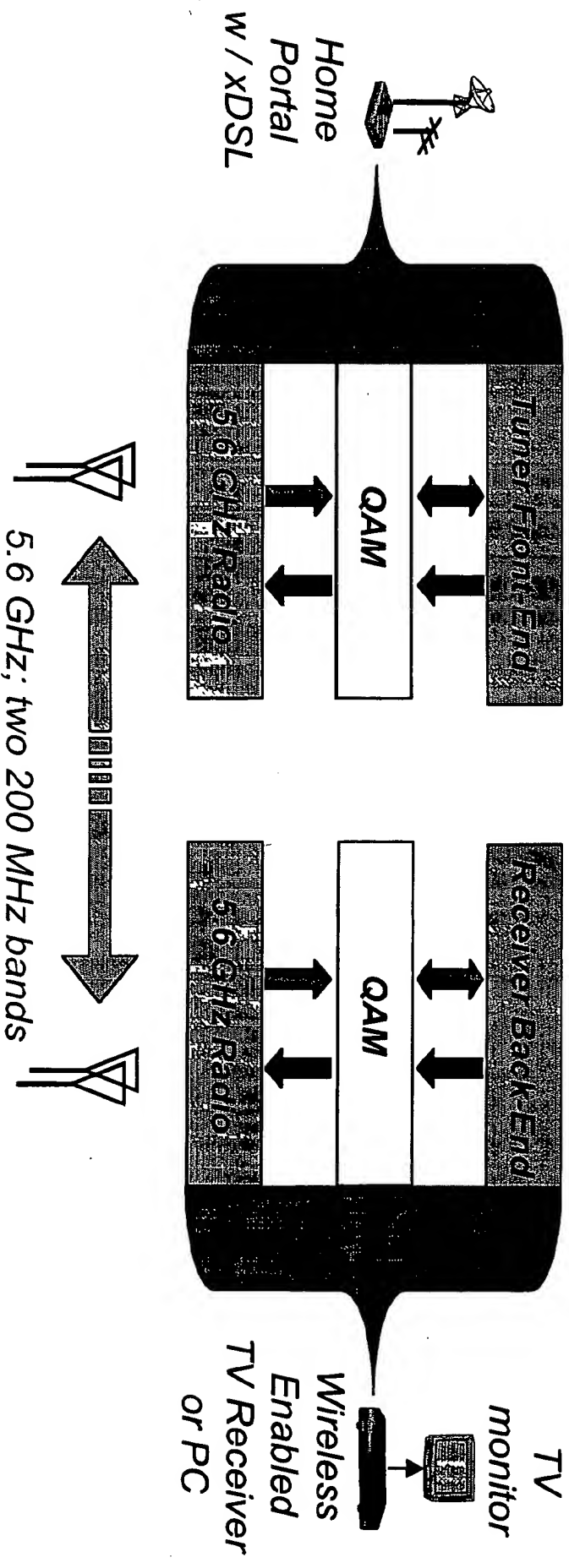**Wireless Camera**

Wireless PC

DBS / ATSC tuner, xDSL Modem

R/W CD / DVD

Wireless CD Sound

HDD

Node #10

Wireless CD Sound

Node #9

Wireless TV Receiver

SDTV Monitor

Node #11

Wireless VideoCam

Wireless Exterior Repeater

In-Home Architecture

FIG. 6

Existing Receiver [STB] Circuits

New RF Circuits

New Logic Circuits

Home Portal w / xDSL

Tuner/Front-End

5.6 GHz Radio

QAM

Receiver Back-End

5.6 GHz Radio

QAM

5.6 GHz; two 200 MHz bands

Wireless Enabled TV Receiver or PC

TV monitor

RF Radio Architecture

FIG 7
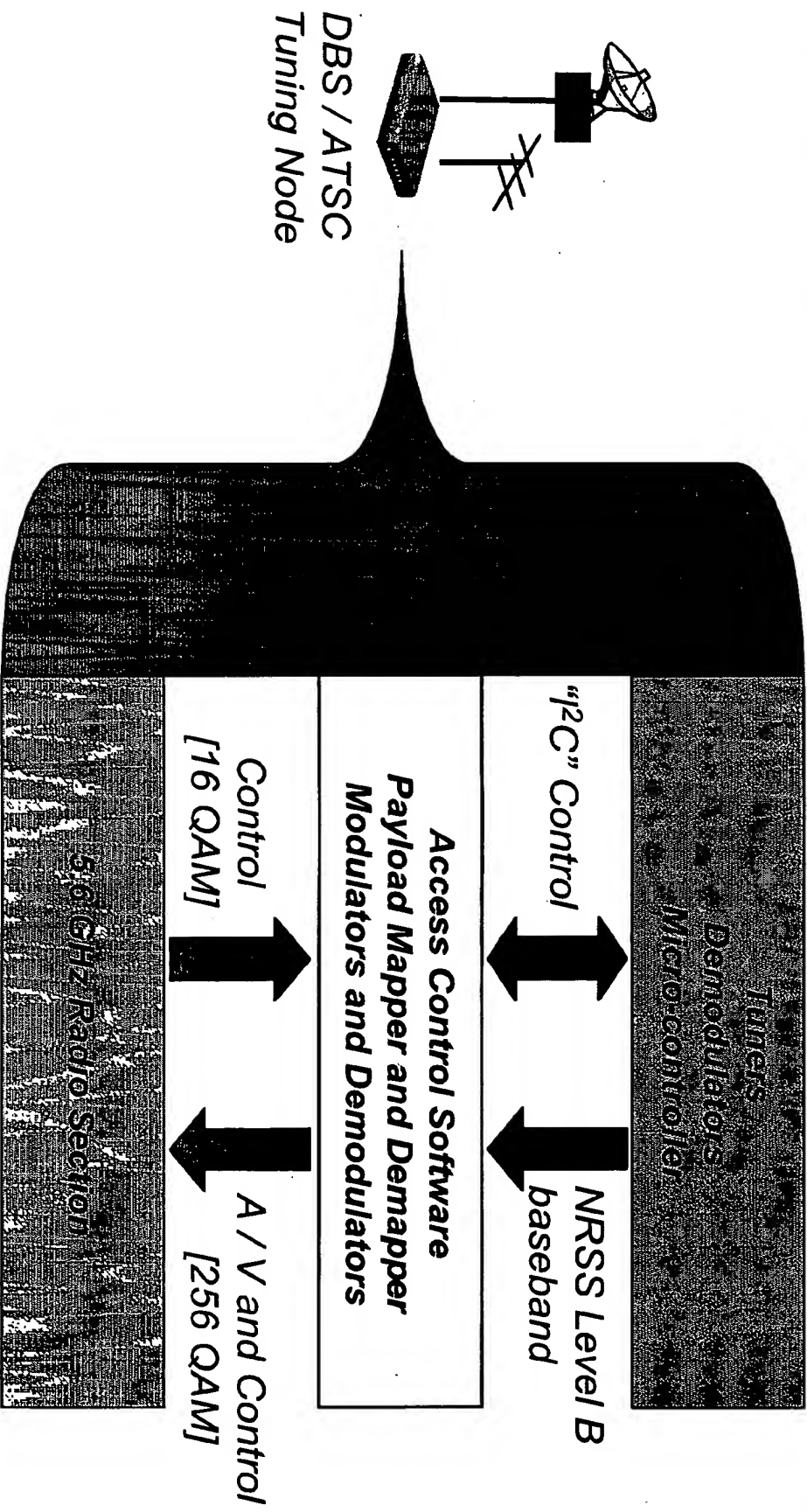
5.6 GHz Broadband
60 two-way channels
CSMA/CD
(carrier sense multiple access with collision detection)

Discrete
Bipolar
Down
Mixer
$ 1.50

UHF

Integrated
Solid State
Tuner
$ 3.00

To Digital
Demodulator

DBS / ATSC Tuner Node

DBS / ATSC Tuning Node

Tuners
Demodulators
Microcontroller

"I2C" Control

NRSS Level B
baseband

Access Control Software
Payload Mapper and Demapper
Modulators and Demodulators

Control
[16 QAM]

A / V and Control
[256 QAM]

5.6 GHz Radio Section

FIG 8

DBS / ATSC Tuner Node

FIG. 9

DBS / ATSC Tuning Node

DBS / UHF / VHF Tuners, QPSK / VSB / NTSC Demodulators Micro-controller

"I2C" Control

NRSS Level B baseband

Air & Physical Interface Access Control Software Layer Payload Mapper / FEC Encoder / 256 QAM Encoder 16 QAM Demod / FEC Decoder / Payload Demapper

Control [16 QAM]

A / V and Control [256 QAM]

5.6 GHz Radio Section

Architecture

40Mb/sec Wireless

(carrier sense multiple access with collision detection)

CSMA/CD

45 two-way channel pairs

5.6 GHz Broadband

Channels

Horizontally and Vertically Polarized Signals
Spatial Diversity Combiner

FIG. 10

DTV Receiver Node

56 GHz Radio Section

Control
[16 QAM] →

← A / V and
Control
[256 QAM]

**Modulators and Demodulators
Payload Mapper and Demapper
Access Control Software**

NRSS Level B
baseband

"I2C"
Control

Conditional Access Module
A / V Decoder
Host Microprocessor

Home Wireless
Receiver

NTSC SDTV

FIG 7/

DTV Receiver Node

56 GHz Radio Section
Modulator / Transmitter / LNB / Radio Tuner / Demodulator

Control
[16 QAM]

A / V and
Control
[256 QAM]

256 QAM Decoder / FEC Decoder / Payload Demapper
Payload Mapper / FEC Encoder / 16 QAM Encoder
Air & Physical Interface Access Control Software Layer

NRSS Level B
baseband

"I2C"
Control

Conditional Access Module
A/V Decoder
Host Microprocessor

Home Wireless
Receiver

NTSC SDTV

FIG 12

QAM Meets System Requirements   FIG-13

» **Data rate:** For handling DBS HDTV feeds, channel data rates must approach 40 Megabits/sec.

» **Low cost** (<$50 retail adder per node): The overall system architecture must intrinsically enable "low cost."

» **Near neighbor interference:** The total number of available channels must be large enough such that "near neighbors" in apartments, town homes, condos, etc., do not interfere with each other.

Why Not COFDM?

FIG 14

» What COFDM does:

 – Robust in dealing with multipath.

 – But COFDM trades-off multipath for increased bandwidth.

» Larger bandwidth:

 – Introduces stringent demands on the RF circuit designer, and the selection of A/D and D/A signal conversion components [phase noise and linearity].

 – Reduces available channels by 50% -- opens the door wider to potential interference between "near neighbors."

» Increased cost:

 – Higher peak power handling for radios.

 – Higher resolution signal converters.

 – Logic gate counts increased by about 1.5X.

Handling In–Home Multipath with QAM

» Increase channel bandwidth on the order of 20%:

– Increasing channel bandwidth from 6MHz to a little over 7MHz may enable demodulators to deal with higher levels of multipath.

» Reduce the modulation system to 128- or 64-QAM:

– Reducing size of symbol constellation increases symbol spacing and thus enhances system's ability to deal with elevated channel noise.

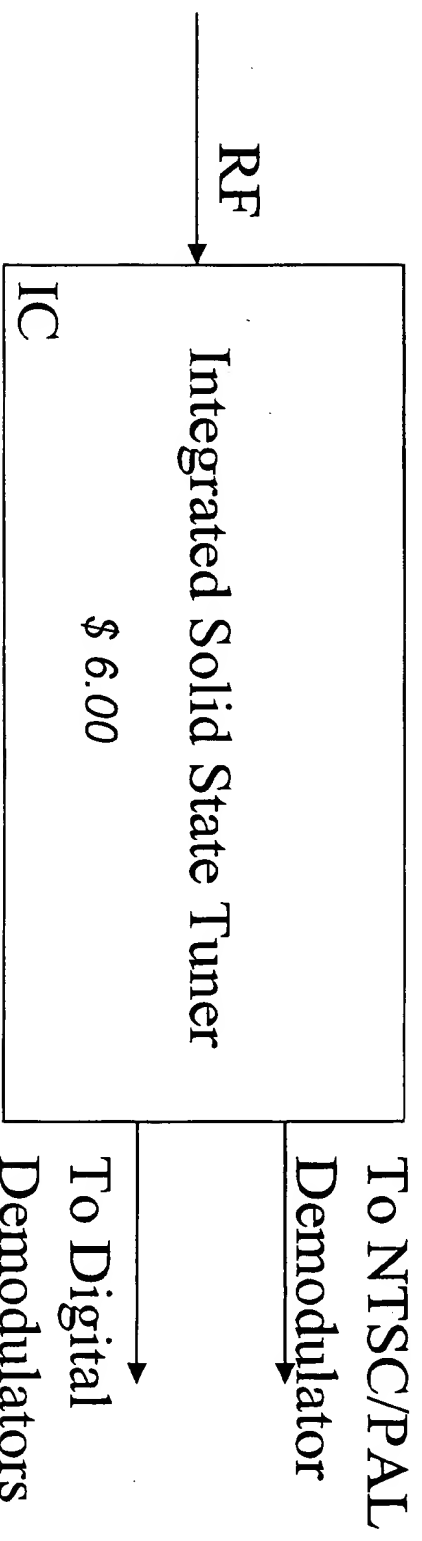– To maintain 40 Mbps channel rates, must increase bandwidth by ~30%.

FIG 15

Spatial Diversity Combiner

FIG 16

Horizontally Polarized

Vertically Polarized

Tuner

Tuner

ADC /14

ADC /14

Timing recovery

Spatial Equalizer

Spatial Equalizer

Tap adjust

Equalizer/ Carrier Loop

Nonlinearity

To FEC
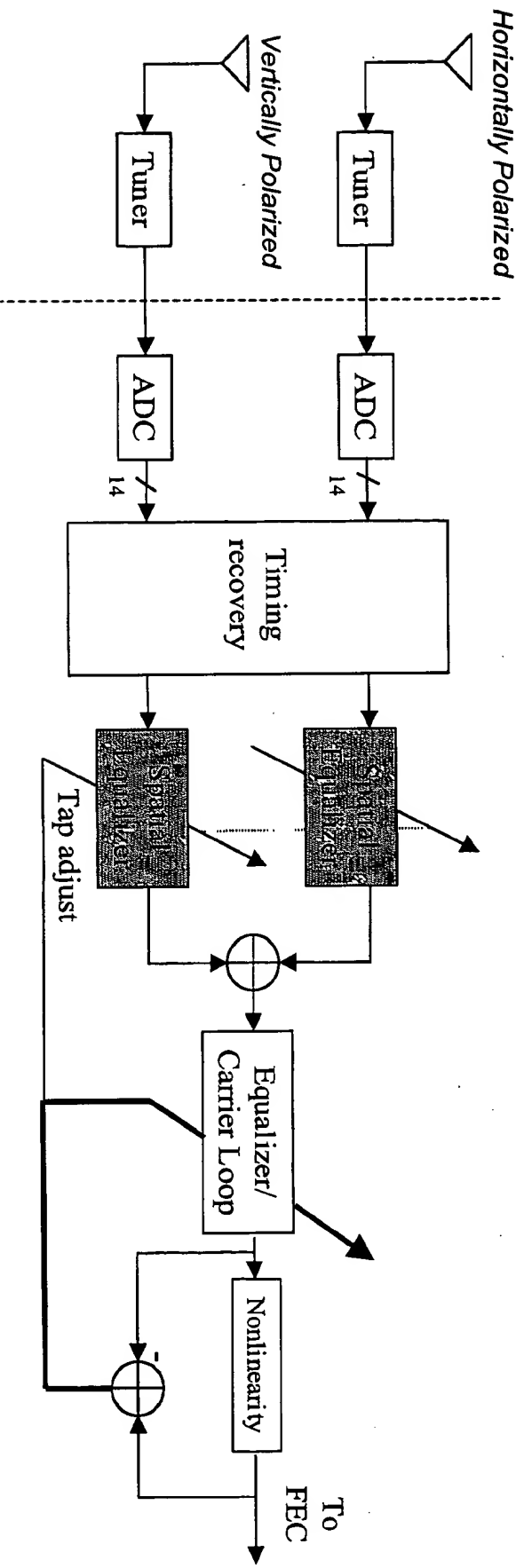
Discrete Tuner Technology

FIG 17

```
                              Digital NTSC/PAL
                              Video & Audio
         Analog          ┌─────────────────┐
         Broadcast       │  NTSC/PAL       │
      ┌────────────────► │  Demodulator    │──────────┐
      │                  │  (ITU-R.656)    │          │
      │                  └─────────────────┘          │
┌─────┴──────────┐                                    │
│ Broadcast/     │        Digital      ┌────────────┐ │
│ Cable          │        Broadcast    │  VSB       │ │
│ Tuner*         │──────────────────►  │ Demodulator│ │ NRSS-
│                │                     └──────┬─────┘ │ Level B
│ * with         │                            │       ▼
│ extensions     │                            │  ┌──────────────┐
│ to QPSK        │                            │  │ ATSC         │
│                │                     NRSS-  │  │ Transport,   │── Video ─►
└─────┬──────────┘                     Level B└─►│ Video, &     │
      │                                          │ Audio        │
      │          Digital       ┌──────┐  NRSS-   │ Decoder      │
      │          Cable         │ QAM  │  Level B │              │
      └───────────────────────►│      │─────────►└──────────────┘
                               └──────┘

    RF ──► [into Broadcast/Cable Tuner]
```
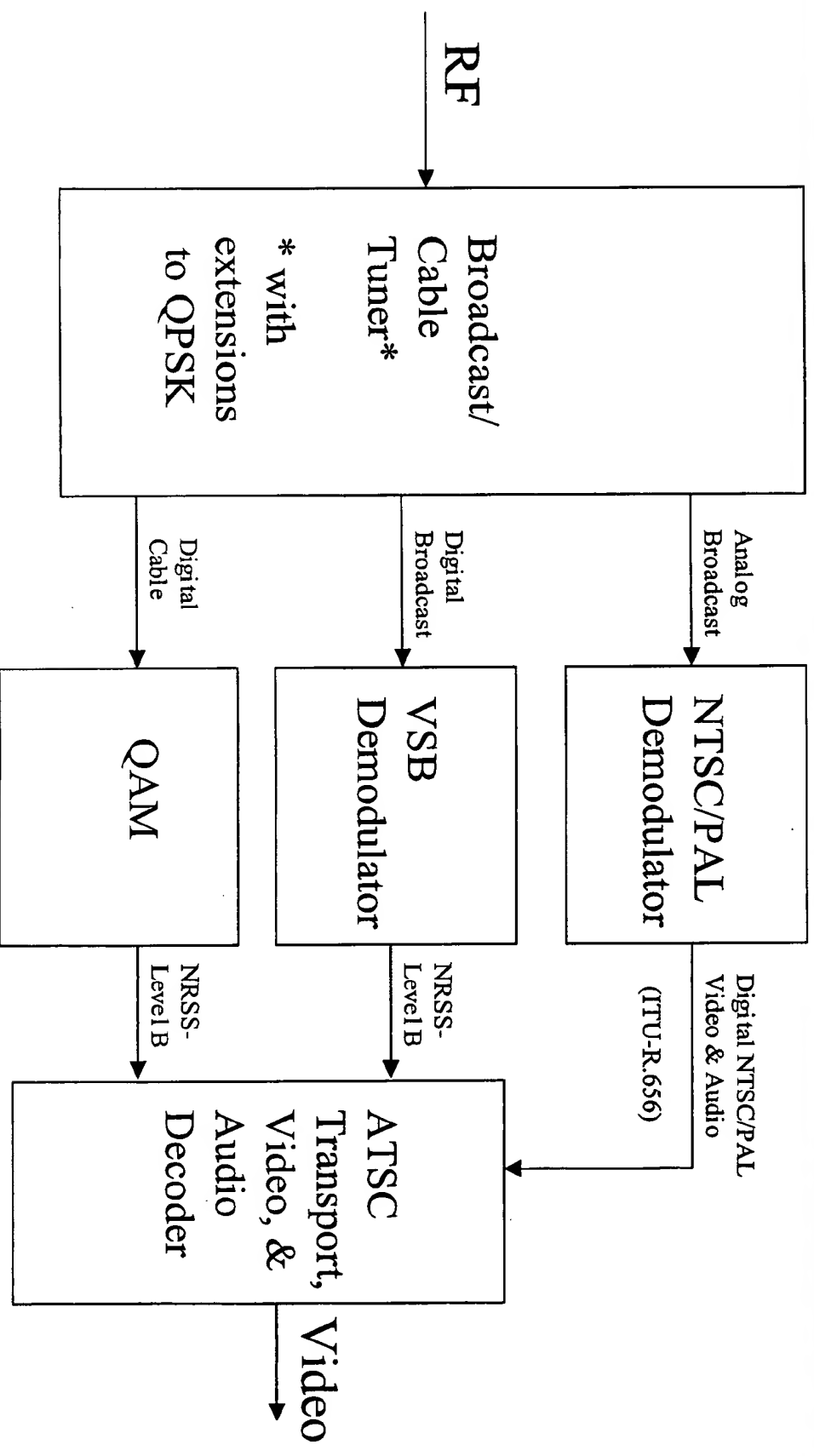
Solid State Tuner

FIG 18

- A Single IC:
  - » RF input and digital outputs to NTSC/PAL and VSB/QAM Digital Demodulators
  - » 2H '00 product introduction

RF →

```
IC
┌─────────────────────────┐
│                         │  → To NTSC/PAL
│ Integrated Solid State  │     Demodulator
│        Tuner            │
│                         │  → To Digital
│       $ 6.00            │     Demodulators
└─────────────────────────┘
```

CMOS Solid State Tuner

FIG. 19

Varactor Tuner

SW 1

Varactor Tuner

Varactor Tuner

VHF low

VHF hi

UHF

Image Reject Mixer

LPF

CMOS Technology

Die Size: 3 x 3 mm

Cost: $3

Wideband A/D

Digital
Multipliers

NCO

NCO

Digital
Filters

Digital
Frequency
Synthesizer

NTSC/PAL
Decoder

D/A

D/A

AGC

I²C

IF Out

Y Out

Cr Out

Audio Out
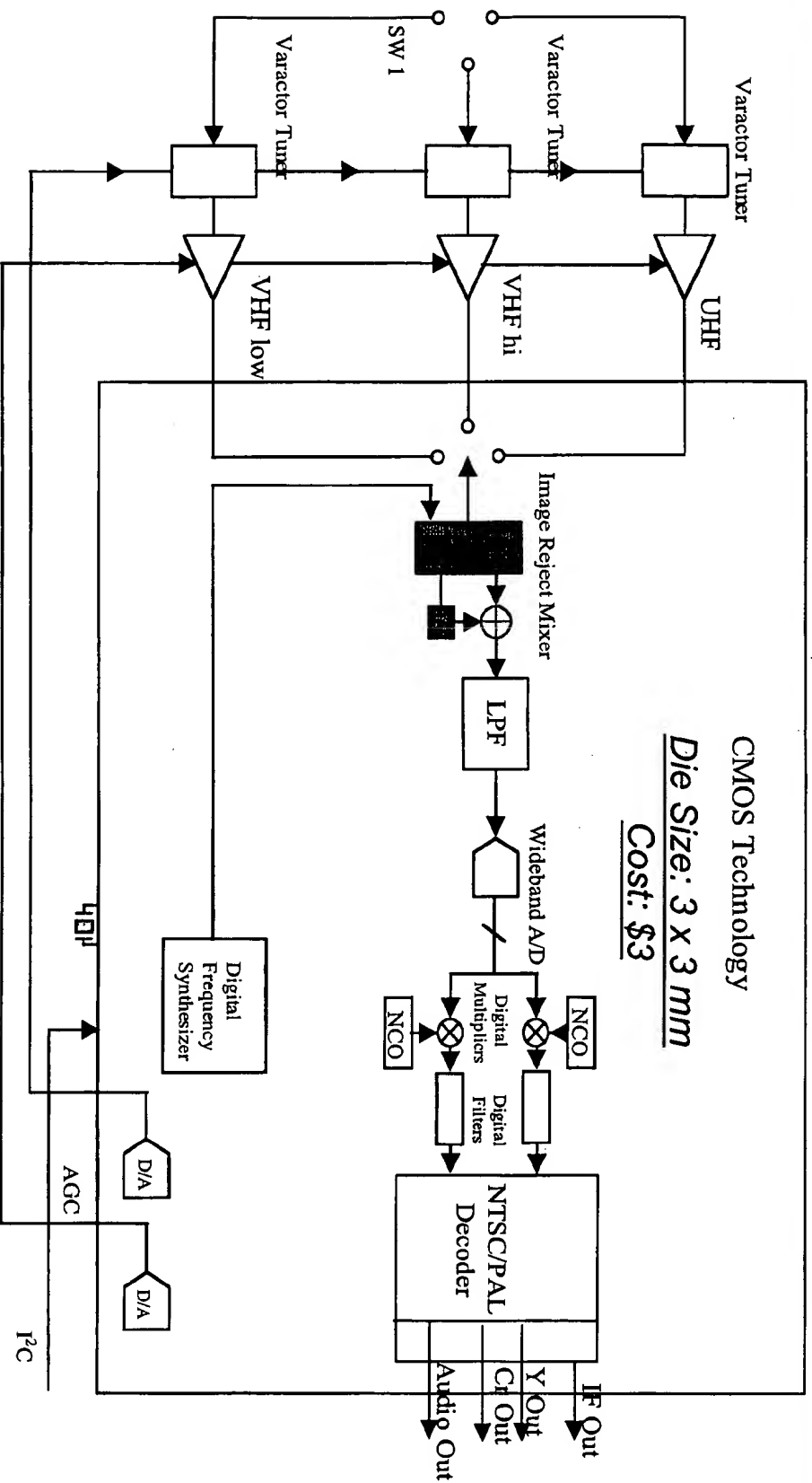
Adding QAM Digital Demodulators

FIG 20

SW 1

Varactor Tuner

Varactor Tuner

Varactor Tuner

VHF low

VHF hi

UHF

CMOS Technology

Image Reject Mixer

Digital
Frequency
Synthesizer

LPF

Wideband A/D

10

NCO

NCO

Digital
Multipliers

Digital
Filters

Digital
Demods

I

Q

9

9

MPEG
Decoder

NTSC/PAL Decoder

D/A

D/A

AGC

I²C

8

Y

Audio

Chroma

Mixer Architecture

BPF

Sin $\omega_1 t$

Cos $\omega_1 t$

BPF

Sin $\omega_2 t = \mathrm{Sin}(2\pi 180 \mathrm{M^6} t)$

Cos $\omega_2 t = \mathrm{Cos}(2\pi 180 \mathrm{E^6} t)$
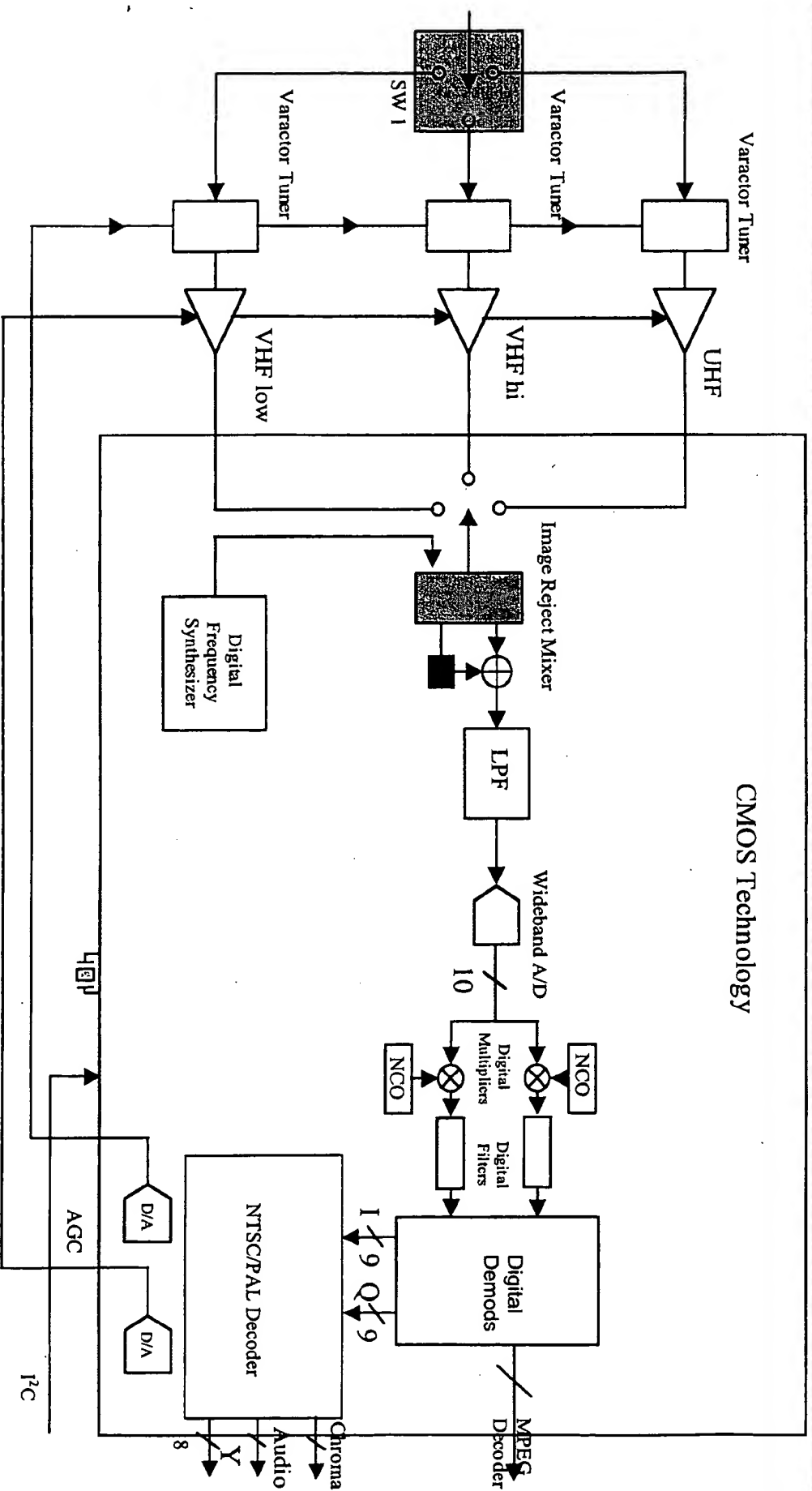
FIG 21
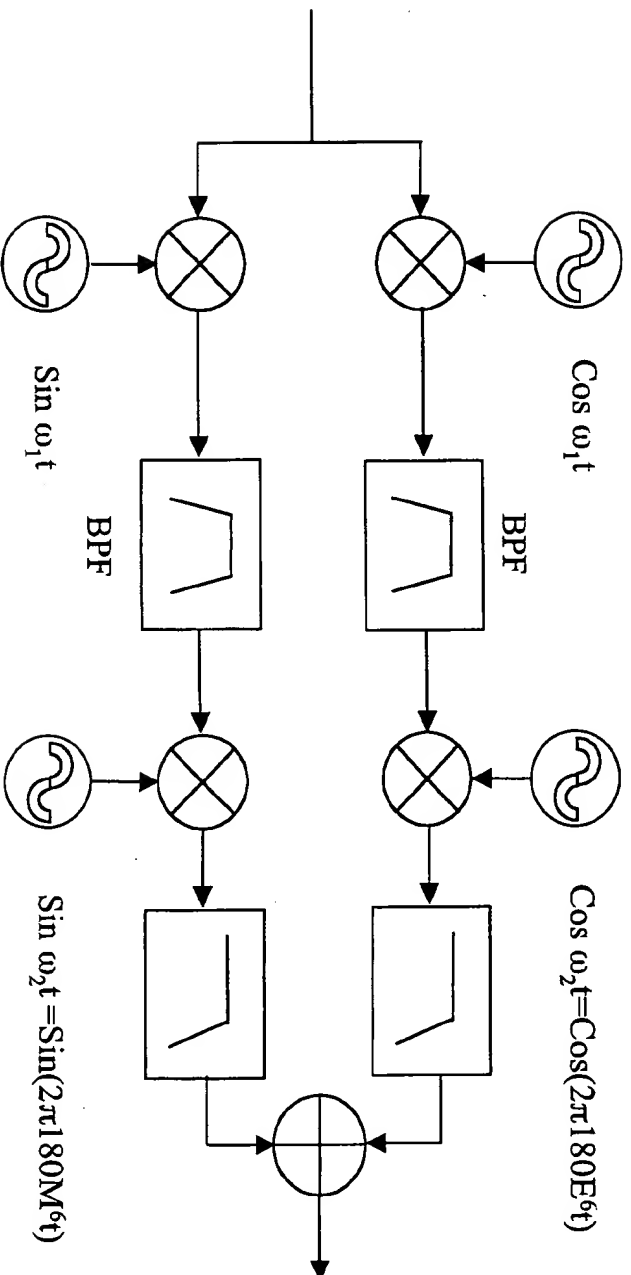
Digital Band Pass Filters

FIG 22

Frequency Response of Digital SAW Filter

freq in mhz

Tuner Mixer
First Silicon Results

FIG 23

Mixer Performance

- +3 dB_m IP_3
  - + 5 to +10 dB_m IP_3 in second silicon

- 35 dB of Local Oscillator reverse isolation

- > 30 dB of Local Oscillator suppression

- > 60 dB of on channel suppression

FIG 24

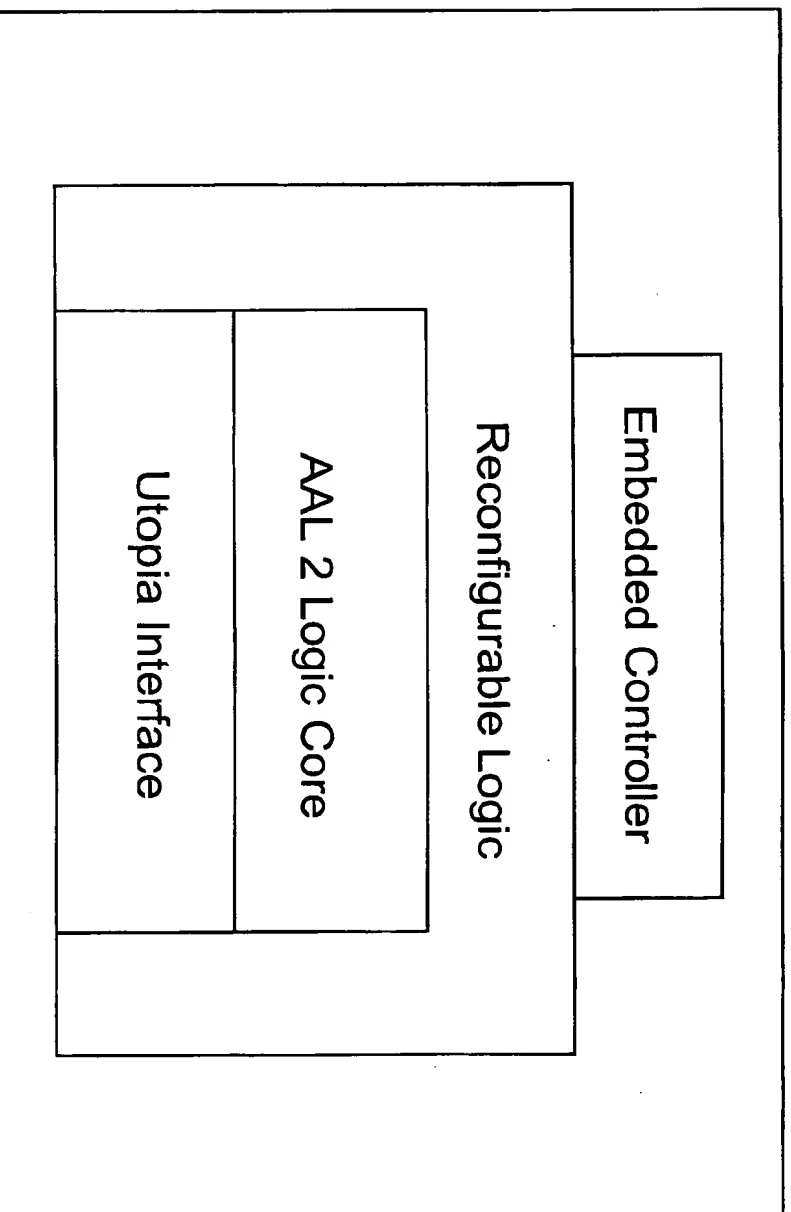# Mixer Performance

- Conversion Capability: beyond 1.1 GHz

- Conversion Gain: 5 - 10dB

- ESD on RF Pads: 2kV

FIG 25

Reconfigurable ATM Adaptation Layer 2 Router

Embedded Controller

Reconfigurable Logic

AAL 2 Logic Core

Utopia Interface

FIG 26

Appendix I

Reconfigurable ATM Adaptation Layer 2 Router (Continued)

FIG. 27

• The AAL 2 core is the heart of the RAAL 2 Router

• Programmable, or reconfigurable, logic surrounds the core

• Programming is accomplished through the embedded processor unit

• As the home network changes and/or market trends drive hardware and networking technologies, the RAAL 2 device can adapt and scale its capability meet a wider range demands.